

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,129,785 B1  
APPLICATION NO. : 10/826464  
DATED : October 31, 2006  
INVENTOR(S) : Christopher D. Nilson

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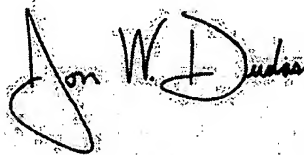
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 8, line 56, "transistor:" should be replaced with --transistor;--.

In column 10, lines 21-22, "circuits the biasing circuit being coupled to a gate of the first transistors wherein" should be replaced with --circuit, the biasing circuit being coupled to a gate of the first transistor, wherein--.

Signed and Sealed this

Twentieth Day of February, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, circular embossed seal of the United States Patent and Trademark Office.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*